

L Number	Hits	Search Text	DB	Time stamp
-	1	6061367.pn.	USPAT;	2004/04/28
-	4	6061367.URPN.	EPO	11:22
-	3	("5361373" "5485629" "5559975").PN.	USPAT	2004/04/15
-	1	0417861\$.did.	USPAT;	13:58
-	2	ep-417861-\$.did.	EPO	2004/04/15
-	2	de-19614991-\$.did.	USPAT;	14:20
-	73	rALU	USPAT;	2004/04/15
-	58	rALU	EPO	15:11
-	33	rALU and g06f\$.ipc.	USPAT;	2004/04/15
-	3	rALU with \$2configur\$6	EPO	15:12
-	49	("5946474" "4884228" "4928306" "5630161" "6195774" "5583450" "6061367" "6061367" "5742504" "5426462" "6134631" "5754823" "6046603" "5497498" "5914666" "5561826" "5592633" "6370323" "6115772" "5353490" "5911055" "5760602" "5652711" "6076152" "5222062" "5226040" "5257393" "5280586" "5301275" "5319754" "5469545" "5574945" "5590284" "5600801" "5703742" "6157975" "6243767" "6247110" "6411301" "6421058" "6424348" "6452600" "6456290" "6466218" "6477610" "6489963" "6597892" "5778439" "6002340" "5555364").pn.	USPAT;	2004/04/15
-	210	dynamic\$5 near2 \$2configur\$4 with (instruction\$1 command\$1)	EPO	15:18
-	152	dynamic\$5 near2 \$2configur\$4 with (instruction\$1 command\$1) and g06f\$.ipc.	USPAT;	2004/04/15
-	13	dynamic\$5 near2 \$2configur\$4 with (instruction\$1 command\$1) and 713/\$.ccls.	EPO	15:20
-	15	dynamic\$5 near2 \$2configur\$4 with (instruction\$1 command\$1) same fpga	USPAT;	2004/04/16
-	251	block\$1 near2 \$2configur\$4 with (instruction\$1 command\$1)	EPO	09:23
-	166	block\$1 near2 \$2configur\$4 with (instruction\$1 command\$1) and g06f\$.ipc.	USPAT;	2004/04/16
-	11	block\$1 near2 \$2configur\$4 with (instruction\$1 command\$1) and 713/\$.ccls.	EPO	09:09
-	117	block\$1 near2 \$2configur\$4 with (instruction\$1) and g06f\$.ipc.	USPAT;	2004/04/16
-	5	block\$1 near2 \$2configur\$4 with (instruction\$1) and 713/\$.ccls.	EPO	09:21
-	0	block\$1 near2 \$2configur\$4 with (instruction\$1)	USPAT;	2004/04/16
-	80	identif\$5 near5 FPGA	EPO	12:54
-	19	identif\$5 near5 FPGA with configur\$6	USPAT;	2004/04/16
-	5	identif\$5 near5 FPGA same CLB\$2	EPO	12:55
			USPAT;	2004/04/16
			EPO	12:56
			USPAT;	2004/04/16
			EPO	12:56
			USPAT;	2004/04/16
			EPO	13:28
			USPAT;	2004/04/16
			EPO	13:29
			USPAT;	2004/04/27
			EPO	15:48
			USPAT;	2004/04/27
			EPO	14:59
			USPAT;	2004/04/27
			EPO	15:49

-	64	partial\$4 near3 \$3configur\$6 with (FPGA CLB\$1)	USPAT; EPO	2004/04/29 08:32
-	9	partial\$4 near3 \$3configur\$6 with (FPGA CLB\$1) same availab\$6	USPAT; EPO	2004/04/29 08:33
-	21	portion\$3 near3 \$3configur\$6 with (FPGA CLB\$1) same availab\$6	USPAT; EPO	2004/04/29 12:28
-	0	un\$1roll\$4 same FPGA	USPAT; EPO	2004/04/29 12:28
-	530	un\$1roll\$4 with loop\$1	USPAT; EPO	2004/04/29 12:28
-	390	un\$1roll\$4 near3 loop\$1	USPAT; EPO	2004/04/29 13:34
-	12	un\$1roll\$4 near3 loop\$1 and FPGA	USPAT; EPO	2004/04/29 12:39
-	132	FPGA with loop\$4	USPAT; EPO	2004/04/29 12:39
-	51	FPGA with loop\$4 and g06f\$.ipc.	USPAT; EPO	2004/04/30 09:34
-	37	FPGA with loop\$4 with \$3configur\$6	USPAT; EPO	2004/04/29 12:39
-	1	loop\$1 adj un\$1roll\$4 with \$3configur\$6	USPAT; EPO	2004/04/29 13:50
-	10	knapp-justin.xa.	USPAT; EPO	2004/04/29 13:50
-	19	\$3configur\$6 near5 FPGA with loop\$4	USPAT; EPO	2004/04/30 09:34